



PTO/SB/08A (08-03)

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 3

Complete if Known

Application Number	10/798,597
Filing Date	March 10, 2004
First Named Inventor	Xiaobao Wang
Art Unit	Unassigned
Examiner Name	Unassigned
Attorney Docket Number	015114-054911US

U.S. PATENT DOCUMENTS*

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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	2	US-6,466,063 B2	10-15-2002	Chen	
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9/1/05

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60294322 v1



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			First Named Inventor	Xiaobao Wang	
			Art Unit	Unassigned	
			Examiner Name	Unassigned	
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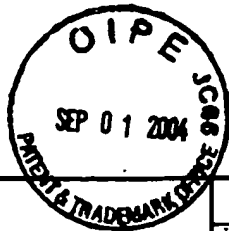
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Examiner Initials*	Cite No. ¹	Document Number Number Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				

Examiner Signature		Date Considered	9/1/05
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PTO/SB/08B (08-03)

Substitute for form 1449B/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/798,597
				Filing Date	March 10, 2004
				First Named Inventor	Xiaobao Wang
				Art Unit	Unassigned
				Examiner Name	Unassigned
Sheet	3	of	3	Attorney Docket Number	015114-054911US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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<i>De</i>	46	"Apex II Programmable Logic Device Family," product data sheet, version 1.1, May 2001 Altera Corporation San Jose, CA (2001), pp. 1-8, pp. 38-59	
<i>De</i>	47	"Virtex-II 1.5V Field Programmable Gate Arrays," product specifications DSO3102 (v1.5) April 2, 2001, Xilinx, Inc., San Jose, CA (2001), pp. 1-36	
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Examiner Signature	<i>Paul D. Chany</i>	Date Considered	9/1/05
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